SIFICATION	P	
Class Subclass Class Subclass	S. UTILITY Patent Application	DATE
JC:3	O.I.P.E.	
APPLICATION NO. CONT/PRIOR CL	ASS SUBCLASS ART UNIT 2815 2816 2826	EXAMINER Dicker
09/756864 Yoshinori Tanaka		
Semiconductor devi	te comprising capacitor and	method of fabrication pro-20%0
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ORIGINAL	ISSUING CLASSIFICATION CROSS REF	ERENCE(S) SUBCLASS PER BLOCK)
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		Continued on Issue Slip Inside File Jacket CLAIMS ALLOWED
TERMINAL DISCLAIMER	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.	Fotal Claims Print Claim for O.G.
The term of this patent	a) (Assistent Examiner) (Cepts)	NOTICE OF ALLOWANCE MAILED
subsequent to has been disclaimed. The term of this patent shall action beyond the expiration of		ISSUE FEE Amount Due Digite Paid
of U.S Patent. No.	(Primary Examiner) (Date	
The terminal months of this patent have been disclaimed	(Legaf instrumenta Examiner) (Da	(a) Code Title 35, Sections 122, 181 and 368.
A Company of the Comp	disclosing may be promised	d contractors only.

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